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Rita Turinella

Signature of Person Mailing Paper or Fee

3-22-06

Date of Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of)	
DAN TOMESCU, et al.)	Examiner: Not Assigned
)	Group Art Unit: 2183
on DATA PROCESSING SYSTEM)	
HAVING A CARTESIAN)	
CONTROLLER)	
Serial No.: 10/728,234)	
Filed on: December 4, 2003)	(Our Docket No. 5227-0001-1)

Mail Stop Amendment
COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 C.F.R. § 1.56, the references set forth below and listed on the attached forms PTO/SB/08A and PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited foreign references and "other documents" are enclosed.

<u>U.S. Patent No</u>	<u>Entitled</u>	<u>Dated</u>
4,780,811	Vector Processing Apparatus Providing Vector and Scalar Processor Synchronization	10/25/1988

<u>U.S. Patent No</u>	<u>Entitled</u>	<u>Dated</u>
6,145,075	Apparatus and Method for Executing A Single-Cycle Exchange Instruction to Exchange Contents of Two Locations in a Register File	11/07/2000

<u>Intl Publ. No</u>	<u>Entitled</u>	<u>Dated</u>
WO 01/33352	Fast Multithreading for Closely Coupled Multiprocessors	05-10-2001

<u>Foreign Patent No.</u>	<u>Country</u>	<u>Dated</u>
GB2230119	Great Britain	10/10/1990

OTHER DOCUMENTS

Intel Architecture Software Developer's Manual - Volume 1: Basic Architecture, Chapter 4 "Procedure Calls, Interrupts, and Exceptions" XP-002363616

Shaw, David Elliott and Sabety, Theodore M.; "Active Memory Chips: A Brief Survey and Case Study", Circuits Systems Signal Process, Vol. 4, No. 2, 1987 - XP-000747239

Berrached, Ali et al., "A Decoupled Access/Execute Architecture for Efficient Access of Structured Data", PA State University, 1993 IEEE

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such a publication is prior art. Moreover, the Applicant understands that the Examiner is to make an independent evaluation of the cited information.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached forms PTO/SB/08A and PTO/SB/08B with initials or other appropriate marks.

No fees are considered to be due; however, if it is determined that payment of a fee is required, please charge our deposit account No. 13-0235.

Respectfully submitted,

By 

Nicholas J. Tuccillo, Esq.
Attorney for Applicant(s)
Registration No. 44,322

McCORMICK, PAULDING & HUBER LLP
CityPlace II, 185 Asylum Street
Hartford, Connecticut 06103-4102
Tel. (860) 549-5290
Fax. (413) 733-4543



PTO/SB/08A (07-05)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/728,234
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Filing Date	December 4, 2003
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First Named Inventor	Gheorghe Stefan
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Art Unit	2183
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Examiner Name

Attorney Docket Number	5227-0001-1
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			
		WO 01/33352 A1	05/10/2001	Advanced Micro Devices		
		GB 2230119 A	10/10/1990	Intel Corporation		

Examiner Signature		Date Considered	
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²EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ³ Applicant's unique citation designation number (optional). ⁴ See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ⁵ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁶ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁷ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁸ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

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Application Number	10/728,234
Filing Date	December 4, 2003
First Named Inventor	Gheorghe Stefan
Art Unit	2183
Examiner Name	
Attorney Docket Number	5227-0001-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		INTEL, Intel Architecture Software Developer's Manual - Vol. 1: Basic Architecture, Chpt. 4, "Procedure Calls, Interrupts, and Exceptions"	
		SHAW, David E. et al., Active Memory Chips: A Brief Study and Case Study", Circuits Systems Signal Process, Vol. 6, NO. 2, 1987	
		BERRACHED, Ali et al., A Decoupled Access/Execute Architecture for Efficient Access of Structured Data, PA State University, 1993 IEEE	

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